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Features

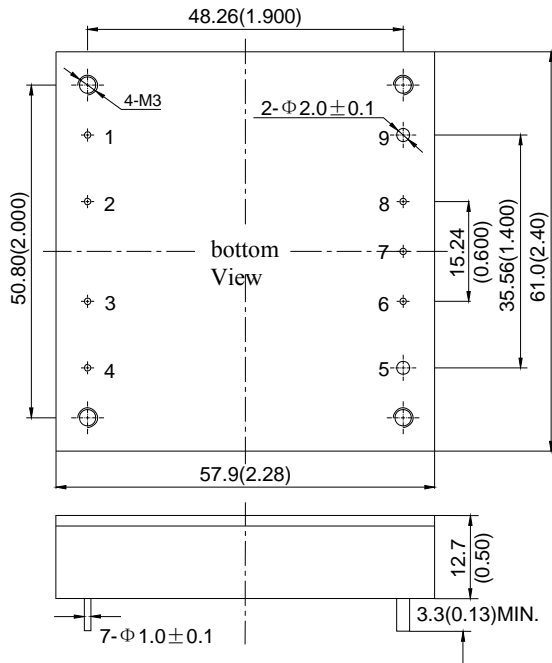
- ◆ Half Brick (57.9mm×61mm×12.7mm)
- ◆ Input Under Voltage Protection (15.0 to 18.0Vdc Turn off)
- ◆ Positive Logic Control (3.5V to 15V or floating Turn on)
- ◆ Output Over Voltage Protection (28.8V to 33.6V)
- ◆ Output Voltage Adjust Range: ±10% of the rated output voltage
- ◆ Output Over Current Protection
- ◆ Output Short-circuit Protection, automatic recovery
- ◆ High Efficiency up to 92.5% (24V, full load)
- ◆ 1500Vdc Isolation Voltage
- ◆ Operation Ambient Temperature -40°C to +85°C
- ◆ 110°C Typ. Over Temperature Protection
- ◆ Baseplate Temperature :-40°C to +100°C
- ◆ Applications: telecommunication applications, electronic data Processing, distributed power architecture, Industry, Rail transit and Railway etc.

Ordering Information

See Contents for individual product ordering numbers.

| Suffix | Description | Ordering No. |
|--------|--|------------------|
| -- | Positive Logic Control: | ZDH200-48BS24 |
| N | Negative Logic Control | ZDH200-48BS24N |
| B | Equipped with metal baseplate. A heatsink can be installed on the baseplate. | ZDH200-48BS24B |
| BC | Equipped with plastic case | ZDH200-48BS24BC |
| NBC | Negative Logic Control. Equipped with metal baseplate and plastic case | ZDH200-48BS24NBC |

Outline Diagram



| Pin | Symbol | Function |
|-----|--------|-----------------------|
| 1 | -Vin | Negative Input |
| 2 | NP | No Pin |
| 3 | CNT | Remote Control Pin |
| 4 | +Vin | Positive Input |
| 5 | +Vo | Positive output |
| 6 | +S | Positive Remote Sense |
| 7 | TRIM | Output voltage adjust |
| 8 | -S | Negative Remote Sense |
| 9 | -Vo | Negative Output |

Notes: all dimensions in mm(inches)
 Tolerances: X.X±0.5mm(X.XX±0.02)
 X.XX±0.25mm(X.XXX±0.010)

Specification

Unless otherwise specified, all values are given at: 25°C, one standard atmosphere pressure, pure resistive load and basic connection.

| Input | Symbol | Min | Typ | Max | Unit | Conditions |
|-------------------------------------|--------------------|------|-------|------|------|--|
| Input Voltage | V _{in} | 18 | 24/48 | 75 | Vdc | — |
| Input Current | I _{in} | — | — | 13 | A | V _{in} =18Vdc, I _O =8.4A |
| Quiescent Input Current | | | 150 | | mA | |
| Positive Logic Remote Control | ON | — | 3.5 | — | V | Refer to -V _{in} Also turn on when CNT floating. |
| | OFF | — | -0.7 | — | V | Refer to -V _{in} |
| | Current | — | — | — | 1.0 | mA |
| Start-up Delay Time | T _{delay} | — | 40 | — | ms | — |
| Under Voltage Threshold | V _{UVLO} | 15.0 | — | 18.0 | Vdc | 50% load test |
| Under Voltage Protection Hysteresis | ΔV _{UVLO} | — | 1.5 | — | Vdc | — |

| Output | Symbol | Min | Typ | Max | Unit | Conditions |
|--|---------------------|-------|-------|-------|------------------|--|
| Output Voltage | V _O | 23.76 | 24.00 | 24.24 | Vdc | V _{intyp} , I _{O,nom} |
| Output Current | I _{O,nom} | — | 30 | — | A | — |
| Output Voltage Adjust Range | V _{trim} | 21.6 | — | 26.4 | Vdc | I _O ≤8.4A Po≤200W |
| Line Regulation | S _V | — | — | ±0.3 | % V _O | V _{in} : 18~75Vdc, I _O =8.4A |
| Load Regulation | S _I | — | — | ±0.5 | % V _O | V _{in} =V _{intyp} , I _O : 0A~8.4A |
| Output Over Voltage Protection Set Point | V _{ov,set} | 28.8 | — | 33.6 | Vdc | V _{in} =V _{intyp} |

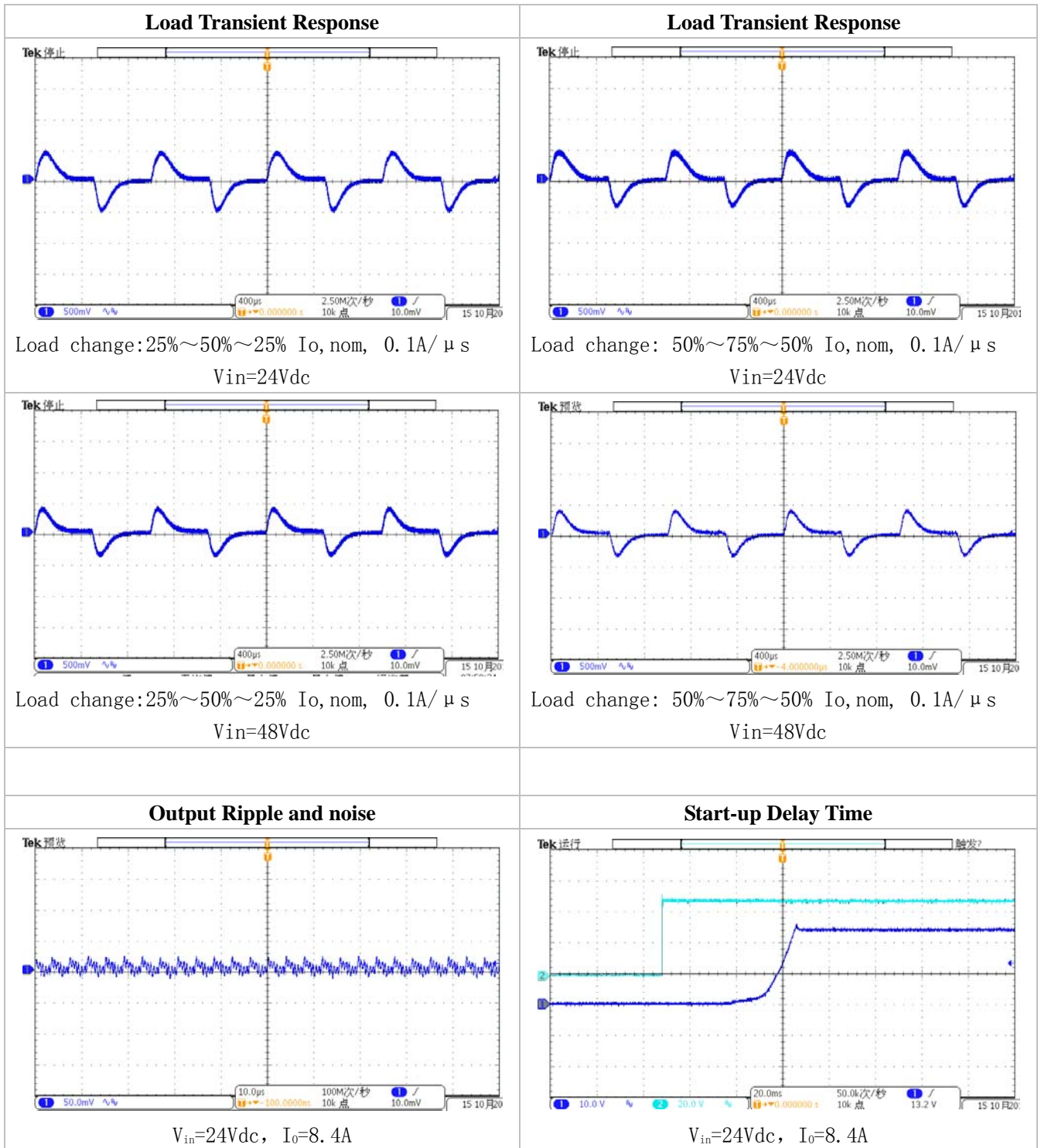
Input 18V-75V, Output 24V/8.4A, Industry Standard Half Brick

Continue

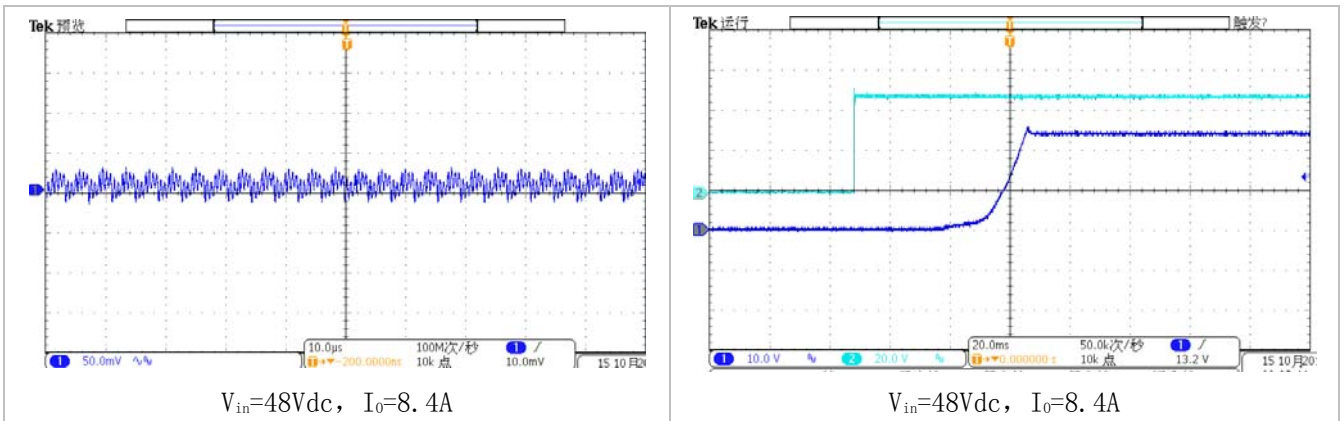
| Output | Symbol | Min | Typ | Max | Unit | Conditions |
|--------------------------------------|--------------------|-----|-----|------|---------|---|
| Output Over Current Protection Range | $I_{O,lim}$ | 9.0 | - | 14 | A | $V_{in}=V_{intyp}$ |
| Output Short-circuit Protection | automatic recovery | | | | | $V_{in}=V_{intyp}$ |
| Peak to Peak Ripple and Noise | ΔV_{pp} | - | - | 200 | mV | $V_{in}=V_{intyp}$, $I_{o,nom}$, 20MHz bandwidth, a 10 μ F Tantalum capacitor and a 1 μ F ceramic capacitor applied at output |
| Rise Time | T_{rise} | - | 16 | - | ms | $V_{in}=V_{intyp}$, $I_{o,nom}$, pure resistive load |
| Output Overshoot | V_{TO} | 0 | - | 2.4 | Vdc | $V_{in}=V_{intyp}$, $I_{o,nom}$, pure resistive load |
| Capacitive Load | C_O | 0 | - | 1000 | μ F | pure resistive load |
| Remote Sense Compensation Range | V_{sense} | 0 | - | 0.5 | V | +S and -S twisted Pair, length is less than 20cm |

| General | | Symbol | Min | Typ | Max | Unit | Conditions |
|---|-------------------|--|------|-----------------|------------|-----------------|--|
| Load Transient | Recovery Time | ΔV_{tr} | - | ± 800 | - | mV | 25%~50%~25% $I_{o,nom}$ or 50%~75%~50% $I_{o,nom}$; 0.1A/ μ s |
| | Voltage Deviation | t_{tr} | - | 200 | - | μ s | |
| Efficiency | | η | 90 | 92.5 | - | % | $V_{in}=V_{intyp}$, $I_{o,nom}$ |
| Switching Frequency | | f_s | - | 240 | - | kHz | - |
| Isolation Resistance | | R_{iso} | 50 | - | - | M Ω | - |
| Isolation Voltage | | V_{iso} | 1500 | - | - | Vdc | Input to output Leak Current: 1mA |
| | | | 1050 | - | - | Vdc | Input to case Leak Current: 1mA |
| | | | 500 | - | - | Vdc | Output to case Leak Current: 1mA |
| MTBF | | - | - | 2×10^6 | - | h | BELLCORE TR-332, |
| Operating Ambient Temperature | | - | -40 | - | +85 | $^{\circ}$ C | See the derating curve |
| Storage Temperature | | - | -55 | - | +125 | $^{\circ}$ C | - |
| Temperature Coefficient | | S_T | - | - | ± 0.02 | %/ $^{\circ}$ C | - |
| Relative Humidity | | - | 10 | - | 90 | % | No condensing, 40 $^{\circ}$ C \pm 2 $^{\circ}$ C |
| Over Temperature Protection Reference Point | | T_{ref} | 105 | 110 | 115 | $^{\circ}$ C | See Over Temperature Protection consideration |
| Over Temperature Protection Hysteresis | | ΔT_{ref} | - | 10 | - | $^{\circ}$ C | |
| Hand Soldering | | Maximum soldering Temperature < 425 $^{\circ}$ C, and duration < 5s | | | | | |
| Wave Soldering | | Maximum soldering Temperature < 255 $^{\circ}$ C, and duration < 10s | | | | | |

Characteristic Curves

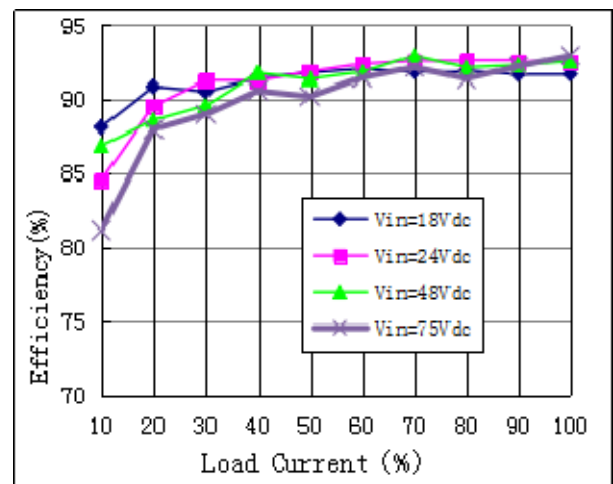
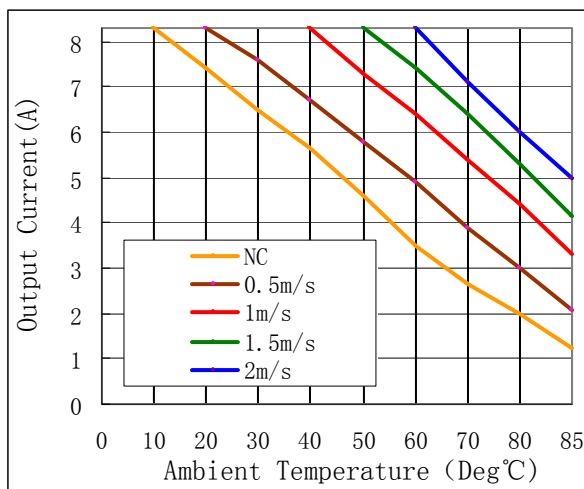


Input 18V-75V, Output 24V/8.4A, Industry Standard Half Brick



Derating

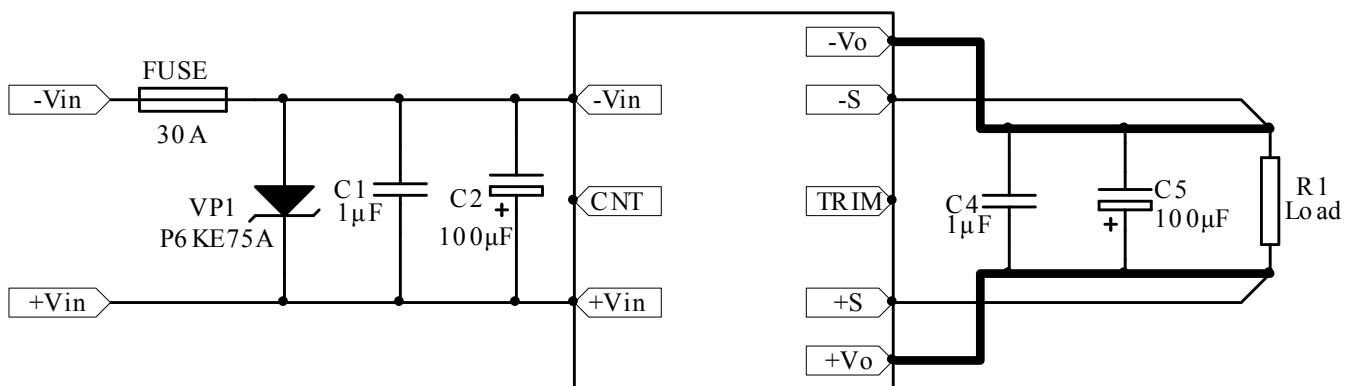
Efficiency vs Io & Vin



$V_{in}=24V$, Without heatsink

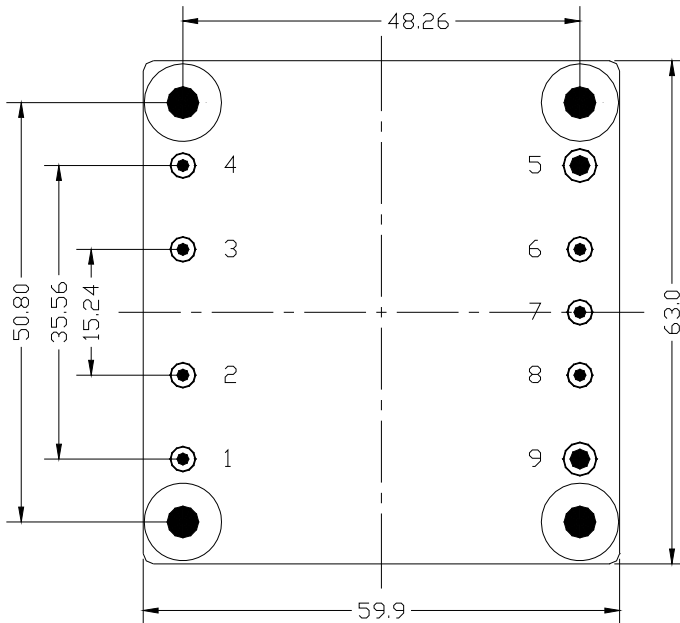
Design Considerations

Basic Connection



Notes: The basic connection indicates the basic requirements that the power module can provide rated output voltage and rated power only. Please refer the instruction followed for further information.

Recommended Layout



| NO. | Recommendation & Notes |
|------------|---|
| Pad Design | 5、9 Pad holes: 2.5mm, pad diameter including hole:5.0mm; 1-4、6-8 Pad holes: 1.5mm, pad diameter including hole:3.3mm; the fixed holes at the four corners are metallized, with diameter of 4.1mm and pad diameter including hole of 8.5mm is keepout layer. |
| Safety | Isolated Converters, care to the spacing between input and output, input and protective ground、output and protective ground. |
| Electrical | The Vin(-) and Vo(-) planes should be placed under of the converter separately. Avoid routing sensitive signal or high disturbance AC signal under the converter. |

Input Voltage Range

The input voltage range of the DC/DC converter is 18V to 75V。 The input impedance of the converter looks like a negative resistor, which can interact with the reactance of the power bus (including any filter elements that have been added to the input of the converter), causes an unstable condition. Depending on the internal transformer’s impedance, the external impedance usually should not exceed the 10% of the internal. So, the source impedance of the Power bus should be kept as low as possible.

The method to determine whether the impedance of the power bus too high or not is to decrease the converter’s input voltage from higher to lower gradually, if the output voltage decreases (unstable sometime) with the lower input voltage, it will be considered the impedance too large. For further confirmation, one electrolytic capacitor can be paralleled to the converter pins after the converter shuts down (one 1μF ceramic capacitor may be required to be paralleled with the electrolytic capacitor), if the output getting better, it will be sure that the impedance is too large.

Remote Control

Remote control can be offered by setting right control voltage level (or floating) to Cnt pin. ZDH200-48BS24 is provided with positive logic remote control. When the level is higher than 3.5V or be left floating, the converter will be turned on. When the level is less than 1.5V, the converter will be turned off. When low voltage level is applied, the output current of the Cnt is less than 5mA. When high voltage level (3.5V~15V) is applied, the input current of the Cnt is less than 5mA

Due to the logic comparator is semiconductor integrated chip, they have low endurance to surge. Care should be taken to prevent Cnt from surge, A TVS should be used in some cases. When the pin is left floating, 2.0V-6.0V voltage appears on the pin.

ZDH200-48BS24N is provided with negative logic remote control. It has the same characteristic as ZDH200-48BS24, except control logic. When the level is higher than 3.5V or be left floating, the converter will be turned off. When the level is less than 1.5V, the converter will be turned on . Like positive logic control converters, care should be taken to prevent Cnt from surge. When the pin is left floating, the voltage of the pin is 2.0V-6.0V.

In some applications, extra controls will be designed for the converter in user’s PCB, such as output short circuit

protection, over voltage protection, under voltage protection, and so on, remote control will give you help. The controls can be achieved by external circuit applied to the Cnt pin.

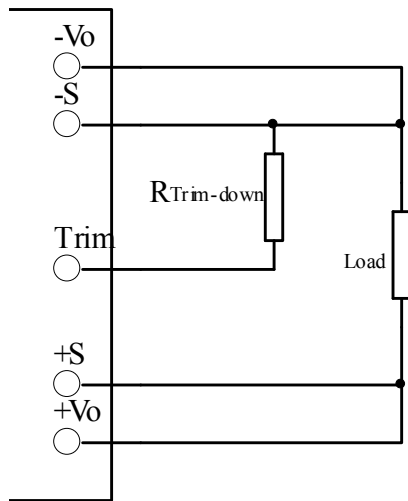
In some applications it is necessary to have a precise turn on and turn off level, or the level which can be received has a very narrow range, the aux. circuit will be required. Please contact us for more information.

External Capacitance

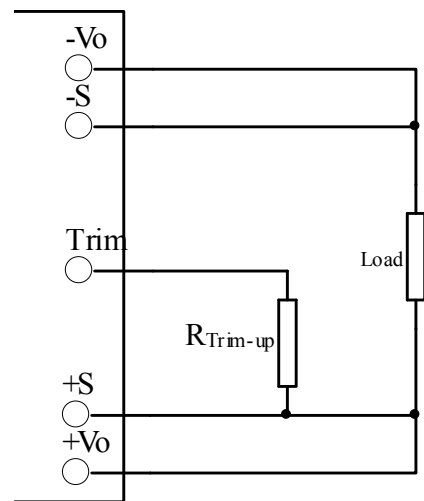
Unless special purpose (i.e. prolonging hold-up time, input impedance matching), the recommended input filter’s capacitance ranges 100µF-220µF, which not only offers a stable system, and reduces the cost, but also lessens the inrush current when the power supplies.

When larger capacitance is required, a circuit of suppressing the inrush current is recommended when the regulator start-up and a discharge circuit is recommended when the output dropped, ensuring the reliability and safety of other equipments in the system.

Output Voltage Adjust



Connection for Trimming Down



Connection of Trimming Up

The converters have an Output Voltage adjust pin (Trim). This pin can be used to adjust the output voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense offset) must be kept below the maximum output adjust range, or the characteristics will not be assured in compliant with the specification, even the over voltage protection may be triggered. Also note that at increased output voltages the maximum power rating of the converter 200W remains the same, and the output current capability will decrease correspondingly, at decrease output voltages the maximum current should not exceed 8.4A. When the trim pins are not used, they should be floated

External circuit is connected as the figure shown, the resistance is calculated as the formula below, please note that

the formula will be invalid when $R_{Trim-up}$ 、 $R_{Trim-down}$ are used simultaneously, users adjust the value based on the resistance applied,

$$R_{Trim-up} = \left(\frac{5.11 \times Vo(100(\%) + \Delta(\%))}{1.225 \times \Delta(\%)} - \frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22 \right) (k\Omega)$$

Resistance for trimming up :

$$R_{Trim-down} = \left(\frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22 \right) (k\Omega)$$

Resistance for trimming down:

V_o : rated output voltage, 5V;

$R_{Trim-up}$ 、 $R_{Trim-down}$: Resistance for trimming up or down, $k\Omega$;

Δ (%): Change rate, divide output voltage by rated output voltage

For example, trimmed down voltage to 22.8V, then Δ (%) = $\left[(24 - 22.8) / 24 \right] \times 100\% = 4\%$;
 $R_{Trim-down} = 5.11 \times 100\% / 4\% - 10.22 = 91.98(k\Omega)$, it can be taken as 90.9 $k\Omega$ 或 93.1 $k\Omega$.

Remote Sense

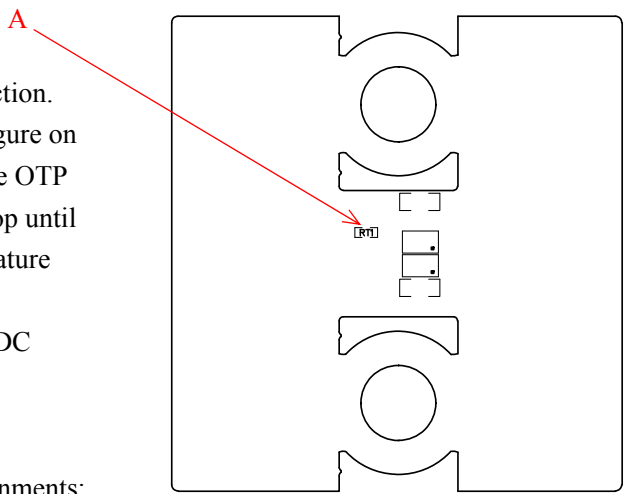
The remote sense can be used to compensate for the voltage drop between the output pins of the converter and the load input pins by +S、-S pins. The +S and -S pins should be connected to the input pins of the load respectively. The remote sense circuit will compensate for up to 10% voltage drop between the sense voltage and the voltage at the output pins. If the remote sense is not needed, the -S should be connected to -Vout and +S should be connected to +Vout.

The anti-interference design should be considered when the +S、-S pins are connected to the pins to be compensated. The +S、-S traces should be located close to a ground trace or ground plane, and the area they surrounded should be minimized (just for electrical isolation); If cable connection presents, twisted pair wires should be used, EMI core are equipped with the twisted pair wires to reduce common mode noise when necessary, the sense leads should not be longer than 200mm, or the system characteristics may not be assured.

The sense leads only can carry very little current, and are not used for converter power output. Care should be taken in operation to avoid damaging the converter.

Over Temperature Protection

The converters provides over temperature protection function. When the PCB temperature (reference point A, see the figure on the right) exceeds the temperature trig point (110 °C), the OTP circuit will cut down output power. The converter will stop until safe operating temperature is restored. Hysteresis temperature between OTP trig point and restart is approx 10°C. Time between OTP and restart is dependent on cooling of DC/DC converter.



Temperature Reference Point A

Thermal Consideration

The converters operate in a variety of thermal environments; however, sufficient cooling should be provided to ensure reliable operation of the unit. Heat is removed by conduction, convection and radiation to the surrounding environment. For the specified ambient temperature, user can increase airflow and change the size of heatsink to improve the heat dissipating for the module with baseplate; user can only increase airflow to improve the heat dissipating for the models without heat sink. Note that the natural convection condition means that airflow is 0.1m/s.

Output Over Voltage Protection

The converter is designed with clamped over voltage protection, when output voltage exceeds 120% to 140% of the

Input 18V-75V, Output 24V/8.4A, Industry Standard Half Brick

rated output voltage (the set point is between 120% to 140%, there is the difference based on the specific parameters, but not beyond the range), the output voltage will be clamped. Be advised that to shut down the converter by using remote control if it can not be repaired timely. Avoid the continuous resetting of the unit because that will damage the converter.

Safety Consideration

The converter, as a component for the end user, should be installed into the equipment, and all the safety considerations are achieved under certain condition. It is required to meet safety requirements in system design. The converter output is considered SELV, and the expected input is considered TNV2, the primary to secondary is basic insulation to EN60950. The maximum operating temperature for PCB is 170 °C.

To avoid fire and be protected when short circuit occurred, it is recommended that a fast blow fuse with rating 2.5 to 3 times of converter's continuous input peak current is used at the input terminal.

Series and Parallel Operation

The converters should not be paralleled directly to increase power, but they can be paralleled each other through o-ring switches or diodes. Make sure that every converter's maximum load current should not exceed the rated current at anytime if they are paralleled without using external current sharing circuits. For the case that there is no external current sharing circuit, but power needs to be increased, please use Trim pin to adjust each converter's output voltage, to load the current as equal as possible in operating (When the load changes in a wide range, the method will not work).

The converters can operate in series. To prevent against start-up failure due to start up time difference, SBD with low voltage difference can be paralleled at the output pins(SBD negative terminal connect to the positive pin of the output) for each converter.

ESD Control

The converters are processed and manufactured in an ESD controlled environment and supplied in conductive packaging to prevent ESD damage from occurring before or during shipping. It is essential that they are unpacked and handled using an ESD control procedures. Failure to do so affects the lifetime of the converter.

Quality Statement

The converters are manufactured in accordance with GJB 9001B system requirements, and are monitored 100% by auto-testing system, 100% burn in.

The warranty for the converters is 5-year.

Contact Information

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