Technical Specification V1.0 2017.12

HESION 示信 ZDE20A-24BS5B **DC-DC Converters**

Input 9V-36V, Output 5V/20A, Industry Standard Eighth Brick

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Features

- Eighth Brick 58.4mm×22.8mm×10.0mm, no heatsink, 58.4mm×22.8mm×12.7mm, including heat sink)
- Input Under Voltage Protection (6.0 to 8.9Vdc Turn off)
- ◆ Positive Logic Control (3.5V to 15V or floating Turn on)
- Output Over Voltage Protection (6V to 7V)
- Output Short-circuit Protection, automatic recovery
- ♦ High Efficiency up to 92% (24V, full load)
- 1500Vdc Isolation Voltage
- Operation Ambient Temperature -40 $^{\circ}$ to +85 °C
- ♦ 120 °C Typ. Over Temperature Protection
- ◆ Applications: Telecom& Datacom ,Industry, Rail transit and Railway etc.



Ordering Information

See Contents for individual product ordering numbers.

| Suffix | Description | Ordering No. |
|--------|--|----------------|
| | Positive Logic Control: 3.5V~15V or floating, turn on; 0V~1.5V, turn off | ZDE20A-24BS5 |
| Р | Negative Logic Control | ZDE20A-24BS5P |
| В | Equipped with metal baseplate. A heatsink can be installed on the baseplate. | ZDE20A-24BS5B |
| РВ | Negative Logic Control. Equipped with metal baseplate. A heatsink can be installed on the baseplate. | ZDE20A-24BS5PB |

ZDE20A-24BS5B DC-DC Converters

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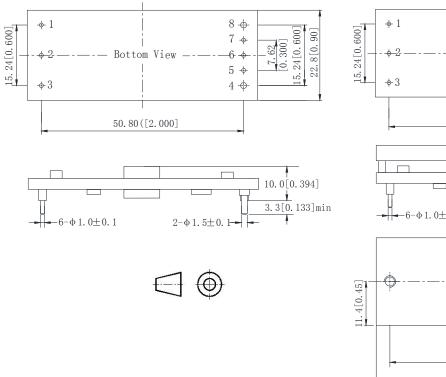
Outline Diagram

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Outline Diagram without Baseplate (without suffix B)

Outline Diagram with Baseplate (with suffix B)

T



| 15.24[0.600] | - & 1 - & 2 Bottor - & 3 | 7 n View —6 5 | |
|--------------|---|---------------------|-----------------|
| | 50.800 | [2.000] | - |
| | 6φ1.0±0.1 | 2-¢1.5±0.1- | 12.7[0.50] |
| 11.4[0.45] | | /iew/ | <u>2-M3×0.5</u> |
| -+ | 51. 31 | [2. 020] | |
| | 58.4[| 2. 30] | |
| | | | |

| Pin | Symbol | Function | | | | | |
|-------------------------------------|--|---|--|--|--|--|--|
| 1 | -Vin | Negative Input | | | | | |
| 2 | Cnt | Remote Control, turn on/off the converter. Output voltage on when Cnt floating or high level applied. | | | | | |
| 3 | +Vin | Positive Input | | | | | |
| 4 | $+V_0$ | Positive Output | | | | | |
| 5 | +S | Positive Remote Sense, connected to $+V_0$ pin when not in use. | | | | | |
| 6 | Trim | Output Voltage Trim, voltage be trimmed up or down by applying external resistor connected to +S or -S output | | | | | |
| 7 | -S | Negative Remote Sense, connected to -V ₀ pin if not used. | | | | | |
| 8 | -V ₀ | Negative Output | | | | | |
| Case Mat | erial: Multil | ayer PCB; Pin: Copper alloy with gold plating; | | | | | |
| Baseplate | e Material: A | luminum | | | | | |
| Notes: All dimensions in mm(inches) | | | | | | | |
| Tolerance | Tolerances:X.X±0.5 (X.XX±0.02) X.XX±0.25 (X.XXX±0.010) | | | | | | |

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Specification

Unless otherwise specified, all values are given at: 25° C, one standard atmosphere pressure, pure resistive load and basic connection.

| Inp | ut | Symbol | Min | Тур | Max | Unit | Conditions |
|--|-------------|--------------------|-----|-----|-----|------|---|
| Input V | oltage | V _{in} | 9 | 24 | 36 | Vdc | — |
| Input C | urrent | I _{in} | - | I | 14 | Α | V _{in} =9Vdc, I ₀ =20A |
| | On | Ι | 3.5 | Ι | 15 | Vdc | Refer to -V _{in} , Turn on when Cnt floating. |
| Negative | Current | - | Ι | 1.0 | - | mA | Cnt source current when turn on |
| Logic Control | Off | Ι | 0 | Ι | 1.2 | Vdc | Refer to -V _{in} ; Turn off when Cnt floating. |
| | Current | I | - | 1.0 | - | mA | Cnt output current when turn off |
| Start-up De | elay Time | T _{delay} | Ι | 10 | - | ms | — |
| Under Voltag | e Threshold | V _{UVLO} | 6.0 | - | 8.9 | Vdc | 50% load test |
| Under Voltage Protection Hysteresis | | | - | 1 | - | Vdc | — |

| Output | Symbol | Min | Тур | Max | Unit | Conditions |
|---|---------------------|------|------|----------|------------|--|
| Output Voltage | Vo | 4.90 | 5.00 | 5.10 | Vdc | — |
| Output Current | I _{O,nom} | - | 20 | - | Α | _ |
| Output Voltage Adjust Range | V _{trim} | 4.5 | Ι | 5.5 | Vdc | Io≤20A Po≤100W |
| Line Regulation | S_V | - | I | ±0.2 | $\% V_{O}$ | $V_{in}: 9 \sim 36 V dc, I_0 = 20 A$ |
| Load Regulation | SI | - | - | ±0.5 | %Vo | V_{in} =24Vdc, I_0 : 0A \sim 20A |
| Output Over Voltage Protection Set Point | V _{ov,set} | 6.0 | - | 7.0 | Vdc | V _{in} =24Vdc |
| Output Over Current Protection Range | I _{O,lim} | 21 | - | 38 | А | Vin=24Vdc |
| Output Short-circuit Protection | | | | automati | c recover | У |
| Peak to Peak Ripple and Noise | $	riangle V_{pp}$ | - | - | 100 | mV | Vin=24Vdc, Io,nom, 20MHz bandwidth, a 10µF Tantalum capacitor and a 1µF ceramic capacitor applied at output |
| Rise Time | T _{rise} | I | 5 | - | ms | V _{in} =24V,I _{O,nom} ,pure resistive load |
| Output Overshoot | V _{TO} | 0 | _ | 0.5 | Vdc | V_{in} =24V,I _{O,nom} , pure resistive load |
| Capacitive Load | Co | 0 | - | 2200 | μF | pure resistive load |
| Remote Sense Compensation Range | V _{sense} | 0 | - | 0.5 | V | +S and -S twisted Pair, length is less than 20cm |

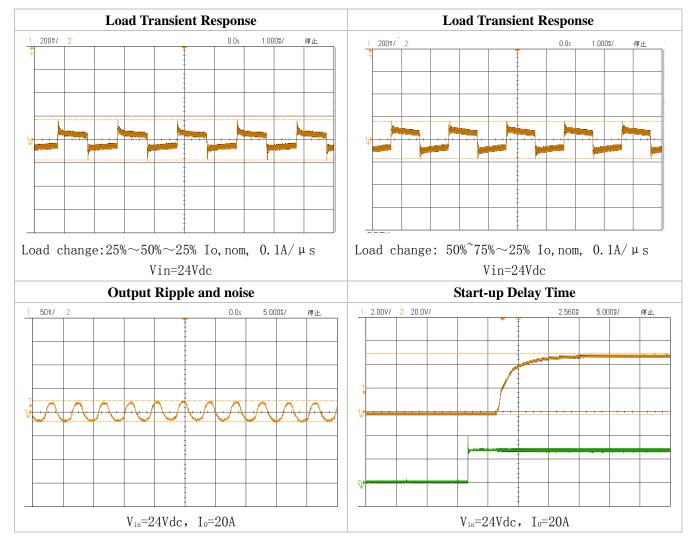
| Gen | eral | Symbol | Min | Тур | Max | Unit | Conditions |
|----------------------|----------------------|-------------------|-----|------|-----|------|--------------------------------|
| Load | Recovery Time | $	riangle V_{tr}$ | | ±250 | | mV | 25%~50%~25%Io,nom or |
| Transient | Voltage Deviation | t _{tr} | _ | 1500 | _ | μs | 50%~75%~50%Io,nom; 0.1A/µs |
| Effic | iency | η | 90 | 92 | I | % | V _{in} =24Vdc, Io,nom |
| Switching Frequency | | f_s | _ | 230 | | kHz | — |
| Isolation Resistance | | R _{iso} | 50 | - | - | MΩ | — |

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Input 9V-36V, Output 5V/20A, Industry Standard Eighth Brick

| Continue | | | | | | |
|---|---|------|-------------------|-------|------|---|
| General | Symbol | Min | Тур | Max | Unit | Conditions |
| Isolation Voltage | V _{iso} | 1500 | - | - | Vdc | Input to output |
| MTBF | _ | _ | 2×10^{6} | _ | h | BELLCORE TR-332, |
| Operating Ambient Temperature | | -40 | Ι | +85 | °C | See the derating curve |
| Storage Temperature | _ | -55 | - | +125 | °C | — |
| Temperature Coefficient | \mathbf{S}_{T} | _ | | ±0.02 | %∕°℃ | — |
| Relative Humidity | _ | 10 | _ | 90 | % | No condensing, $40^{\circ}C\pm 2^{\circ}C$ |
| Over Temperature Protection Reference Point | T _{ref} | 115 | 120 | 125 | °C | See Over Temperature Protection consideration |
| Over Temperature Protection Hysteresis | $	riangle T_{ref}$ | | 10 | | °C | consideration |
| Hand Soldering | Maximum soldering Temperature $< 425^{\circ}$ C, and duration $< 5s$ | | | | | |
| Wave Soldering | Maximum soldering Temperature $< 255 ^{\circ}\text{C}$, and duration $< 10s$ | | | | | |

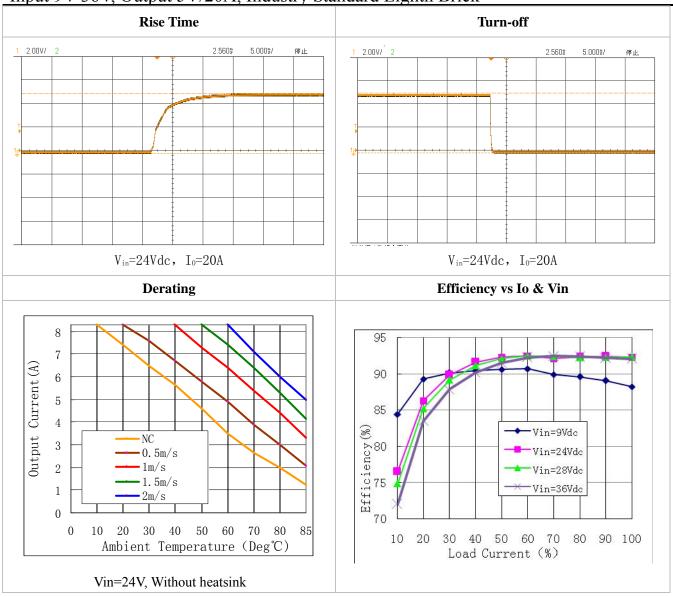
Characteristic Curves



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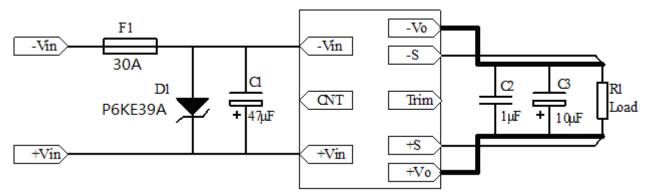
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Design Considerations

Basic Connection

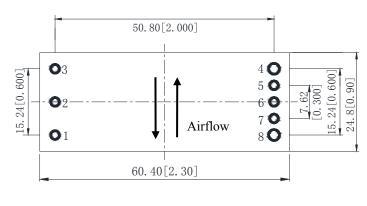


Notes: The basic connection indicates the basic requirements that the power module can provide rated output voltage and rated power only. Please refer the instruction followed for further information.

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Recommended Layout



| NO | | | | | | | |
|------------|---------------------------------------|--|--|--|--|--|--|
| NO. | Recommendation & Notes | | | | | | |
| Pad | Pad hole 4&8: 2.0mm, pad diameter | | | | | | |
| Design | including hole: 3.0mm; the rest are | | | | | | |
| Design | 1.3mm and 2.4 mm respectively. | | | | | | |
| Airflow | As the figure shown or the opposite | | | | | | |
| Direction | direction, perpendicular direction is | | | | | | |
| Direction | not recommended. | | | | | | |
| Safety | Isolated Converters, care to the | | | | | | |
| Safety | spacing between input and output | | | | | | |
| | The Vin(-) and Vo(-) planes should | | | | | | |
| | be placed under of the converter | | | | | | |
| Electrical | separately. Avoid routing sensitive | | | | | | |
| | signal or high disturbance AC signal | | | | | | |
| | under the converter. | | | | | | |

Input Voltage Range

The input voltage range of the DC/DC converter is 9V to $36V_{\circ}$ The input impedance of the converter looks like a negative resistor, which can interact with the reactance of the power bus (including any filter elements that have been added to the input of the converter), causes an unstable condition. Depending on the internal transformer's impedance, the external impedance usually should not exceed the 10% of the internal. So, the source impedance of the Power bus should be kept as low as possible.

The method to determine whether the impedance of the power bus too high or not is to decrease the converter's input voltage from higher to lower gradually, if the output voltage decreases (unstable sometime) with the lower input voltage, it will be considered the impedance too large. For further confirmation, one electrolytic capacitor can be paralleled to the converter pins after the converter shuts down (one 1μ F ceramic capacitor may be required to be paralleled with the electrolytic capacitor), if the output getting better, it will be sure that the impedance is too large.

Remote Control

Remote control can be offered by setting right control voltage level (or floating) to Cnt pin. CDE20A-24BS5 is provided with positive logic remote control. When the level is higher than 3.5V or be left floating, the converter will be turned on. When the level is less than 1.5V, the converter will be turned off. When low voltage level is applied, the output current of the Cnt is less than 5mA. When high voltage level $(3.5V \sim 15V)$ is applied, the input current of the Cnt is less than 5mA.

Due to the logic comparator is semiconductor integrated chip, they have low endurance to surge.Care should be taken to prevent Cnt from surge, A TVS should be used in some cases. When the pin is left floating, 2.0V-6.0V voltage appears on the pin.

CDE20A-24BS5P is provided with negative logic remote control. It has the same characteristic as CDE20A-24BS5, except control logic. When the level is higher than 3.5V or be left floating, the converter will be turned off. When the level is less than 1.5V, the converter will be turned on . Like positive logic control converters, care should be taken to prevent Cnt from surge. When the pin is left floating, the voltage of the pin is 2.0V-6.0V.

In some applications, extra controls will be designed for the converter in user's PCB, such as output short circuit protection, over voltage protection, under voltage protection, and so on, remote control will give you help. The controls can be achieved by external circuit applied to the Cnt pin.

In some applications it is necessary to have a precise turn on and turn off level, or the level which can be received has a very narrow range, the aux. circuit will be required. Please contact us for more information.

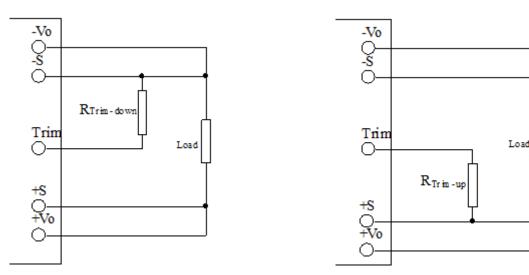
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External Capacitance

Unless special purpose (i.e. prolonging hold-up time, input impedance matching), the recommended input filter's capacitance ranges 47μ F-220 μ F, which not only offers a stable system, and reduces the cost, but also lessens the inrush current when the power supplies.

When larger capacitance is required, a circuit of suppressing the inrush current is recommended when the regulator start-up and a discharge circuit is recommended when the output dropped, ensuring the reliability and safety of other equipments in the system.

Output Voltage Adjust



Connection for Trimming Down

Connection of Trimming Up

The converters have an Output Voltage adjust pin (Trim). This pin can be used to adjust the output voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense offset) must be kept below the maximum output adjust range, or the characteristics will not be assured in compliant with the specification, even the over voltage protection may be triggered. Also note that at increased output voltages the maximum power rating of the converter 100W remains the same, and the output current capability will decrease correspondingly, at decrease output voltages the maximum current should not exceed 20A. When the trim pins are not used, they should be floated

External circuit is connected as the figure shown, the resistance is calculated as the formula below, please note that

the formula will be invalid when $R_{Trim-up}$, $R_{Trim-down}$ are used simultaneously, users adjust the value based on the resistance applied,

$$R_{Trim-up} = \left(\frac{5.11 \times Vo(100(\%) + \Delta(\%))}{1.225 \times \Delta(\%)} - \frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22\right) (k\,\Omega)$$

Resistance for trimming up :

$$R_{Trim-down} = \left(\frac{5.11 \times 100(\%)}{\Delta(\%)} - 10.22\right) (k \,\Omega)$$

Resistance for trimming down:

Vo: rated output voltage, 5V;

 $R_{Trim-up}$, $R_{Trim-down}$: Resistance for trimming up or down, k Ω ;

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 \triangle (%): Change rate, divide output voltage by rated output voltage For example, trimmed down voltage to 4.8V, then \triangle (%) = **[** (5-4.8) /5 **]** ×100%=4%; $R_{\text{Trim-down}}=5.11\times100\%/4\%-10.22=117.53(k\Omega)$, it can be taken as 120 kΩ.

Remote Sense

The remote sense can be used to compensate for the voltage drop between the output pins of the converter and the load input pins by $+S_s$ -S pins. The +S and -S pins should be connected to the input pins of the load respectively. The remote sense circuit will compensate for up to 10% voltage drop between the sense voltage and the voltage at the output pins. If the remote sense is not needed, the -S should be connected to -Vout and +S should be connected to +Vout.

The anti-interference design should be considered when the $+S_{x}$ -S pins are connected to the pins to be compensated. The $+S_{x}$ -S traces should be located close to a ground trace or ground plane, and the area they surrounded should be minimized (just for electrical isolation); If cable connection presents, twisted pair wires should be used, EMI core are equipped with the twisted pair wires to reduce common mode noise when necessary, the sense leads should not be longer than 200mm, or the system characteristics may not be assured.

The sense leads only can carry very little current, and are not used for converter power output. Care should be taken in operation to avoid damaging the converter.

Over Temperature Protection

The converters provides over temperature protection function. When the PCB temperature (reference point A, see the figure on the right) exceeds the temperature trig point (120 °C), the OTP circuit will cut down output power. The converter will stop until safe operating temperature is restored. Hysteresis temperature between OTP trig point and restart is approx 10°C. Time between OTP and restart is dependent on cooling of DC/DC converter.

Thermal Consideration

Temperature Reference Point A

The converters operate in a variety of thermal environments; however, sufficient cooling should be provided to ensure reliable operation of the unit. Heat is removed by conduction, convection and radiation to the surrounding environment. For the specified ambient temperature, user can increase airflow and change the size of heatsink to improve the heat dissipating for the module with baseplate; user can only increase airflow to improve the heat dissipating for the module sink. Note that the natural convection condition means that airflow is $0.05 \sim 0.1$ m/s.

Output Over Voltage Protection

The converter is designed with clamped over voltage protection, when output voltage exceeds 120% to 140% of the rated output voltage (the set point is between 120% to 140%, there is the difference based on the specific parameters, but not beyond the range), the output voltage will be clamped. Be advised that to shut down the converter by using remote control if it can not be repaired timely. Avoid the continuous resetting of the unit because that will damage the converter.

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Safety Consideration

The converter, as a component for the end user, should be installed into the equipment, and all the safety considerations are achieved under certain condition. It is required to meet safety requirements in system design. The converter output is considered SELV, and the expected input is considered TNV2, the primary to secondary is basic insulation to EN60950. The maximum operating temperature for PCB is 170 $^{\circ}$ C.

To avoid fire and be protected when short circuit occurred, it is recommended that a fast blow fuse with rating 2.5 to 3 times of converter's continuous input peak current is used at the input terminal.

Series and Parallel Operation

The converters should not be paralleled directly to increase power, but they can be paralleled each other through o-ring switches or diodes. Make sure that every converter's maximum load current should not exceed the rated current at anytime if they are paralleled without using external current sharing circuits. For the case that there is no external current sharing circuit, but power needs to be increased, please use Trim pin to adjust each converter's output voltage, to load the current as equal as possible in operating (When the load changes in a wide range, the method will not work).

The converters can operate in series. To prevent against start-up failure due to start up time difference, SBD with low voltage difference can be paralleled at the output pins(SBD negative terminal connect to the positive pin of the output) for each converter.

ESD Control

The converters are processed and manufactured in an ESD controlled environment and supplied in conductive packaging to prevent ESD damage from occurring before or during shipping. It is essential that they are unpacked and handled using an ESD control procedures. Failure to do so affects the lifetime of the converter.

Quality Statement

The converters are manufactured in accordance with GJB 9001B system requirements, and are monitored 100% by auto-testing system, 100% burn in.

The warranty for the converters is 5-year.

Contact Information

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