

WHD75-24S24 DC-DC Converter

Input 9V-36V, Output 24V/3.1A, Half-Brick Series

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Features

- ◆ Half-Brick (61.0mm×57.9mm×12.7mm)
- ◆ CE Certificate , RoHS compliant
- ◆ Positive Logic Control (3.5V to 15V turn on)
- ◆ Output Current Limit Protection(OCP)
- ◆ Output Over Voltage Protection (OVP)
- ◆ Output Voltage Adjust Range:±10 % of the rated output voltage
- ◆ Over Temperature Protection (OTP)
- ◆ Output Short-circuit Protection, hiccup, auto-recovery
- ◆ High efficiency, 88%typ(input:24V I_{O,max})
- ◆ 1217Vac Isolation Voltage(Input to output)
- ◆ Baseplate Temperature :-40°C to 100°C
- ◆ Applications: Telecommunications, Electronic Data Processing, Distributed Power Architecture.

Ordering Information

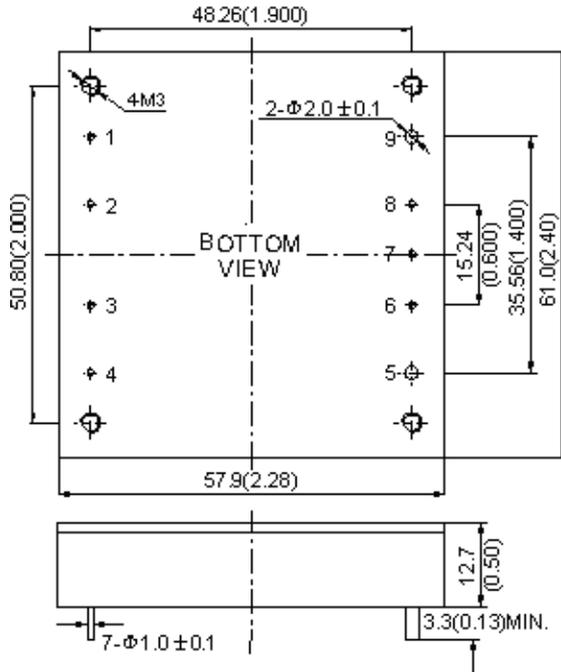
See Contents for individual product ordering numbers.

Suffix	Meaning	Ordering Model
--	Basic Model	WHD75-24S24
P	Negative Logic Control. Turn off when CNT pin is applied to 3.5~15V voltage or kept floating; Turn on when CNT pin is applied to 0~1.5V voltage	WHD75-24S24P

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Outline Diagram



Pin	Symbol	Function
1	-Vin	Negative Input
2	CASE	Connect to the baseplate
3	CNT	Remote Control Pin
4	+Vin	Positive Input
5	+Vo	Positive output
6	+S	Positive Remote Sense
7	TRIM	Output voltage adjust
8	-S	Negative Remote Sense
9	-Vo	Negative Output

Case material: Black flame retardant Plastic; Pins: copper with gold plating
 Aluminum baseplate can be connected to Protective Earth pin by M3 screw.

Notes: all dimensions in mm(inches)

Tolerances: X.X±0.5mm(X.XX±0.02)

X.XX±0.25mm(X.XXX±0.010)

Specifications

Unless otherwise specified, all values are given at: 25°C, one standard atmosphere pressure, pure resistive load and basic connection.

Input	Symbol	Min	Typ	Max	Unit	Conditions	
Input Voltage	V _{in}	9	24	36	V	—	
Start-up Delay Time	T _{delay}	—	5	—	ms	V _{in} =24V, I _{O,max}	
Maximum Input Current	I _{in,max}	—	—	10	A	—	
Positive Logic Remote Control	ON	—	3.5	—	15	V	Refer to -Vin Also turn on when CNT floating.
	OFF	—	0	—	1.5	V	
	Current	—	—	—	1	mA	—

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Output		Symbol	Min	Typ	Max	Unit	Conditions
Output Power		P_o	0	-	75	W	—
Output Voltage		V_o	23.76	24.00	24.24	V	—
Output Current		I_o	-	-	3.1	A	—
Current Limit Inception		$I_{o,lim}$	3.41	—	5.27	A	$V_{in}=24V$
Output Voltage Adjust Range		V_{trim}	-	-	± 10	% V_o	$I_o \leq 3.1A$ $P_o \leq 75W$
Line Regulation		S_V	-	-	± 0.2	% V_o	$V_{in}: 9V \sim 36V, I_{o,max}$
Load Regulation		S_I	-	-	± 0.5	% V_o	$V_{in}=24V, I_o: 0 \sim 100\% I_{o,nom}$
Peak to Peak Ripple and Noise		ΔV_{pp}	-	-	150	mV	20MHz bandwidth, Output equipped 10 μ F tantalum capacitor and 1 μ F ceramic capacitor
Load Transient	Recovery Time	t_{tr}	-	-	200	μ s	Load change: 25%~50%~25% & 50%~75%~50% Current change: 0.1A/ μ s
	Voltage Deviation	ΔV_{tr}	-	-	± 720	mV	
Capacitive Load Range		C_o	0	-	1000	μ F	$V_{in}=24V, I_{o,max}$ Pure resistive load
Rise Time		T_{rise}	-	15	-	ms	$I_{o,max}$, Pure resistive load
Output Overshoot		V_{TO}	-	-	10	% V_o	$V_{in}=24V, I_{o,max}$ Pure resistive load
OVP Set Point		$V_{ov,set}$	28.8	-	33.6	V	$P_o \leq 75W$
OTP Set Point		T_{ref}	115	120	125	$^{\circ}C$	Auto-recovery
Output Short-circuit Protection		Hiccup mode, automatic recovery					

General	Symbol	Min	Typ	Max	Unit	Conditions
Efficiency	η	86	88	-	%	$V_{in}=24V, I_{o,max}$
Switching Frequency	f_s	-	210	-	kHz	—
Isolation Resistance	R_{iso}	50	-	-	M Ω	—
Isolation Voltage	V_{iso}	1217	—	—	Vac	Input to output Leak Current: 5mA
		1050	—	—	Vdc	Input to case Leak Current: 1mA
		500	—	—	Vdc	Output to case Leak Current: 1mA
Operating Baseplate Temperature	-	-40	-	100	$^{\circ}C$	—
Storage Temperature	-	-55	-	125	$^{\circ}C$	—
Temperature Coefficient	S_T	-	-	± 0.02	%/ $^{\circ}C$	—

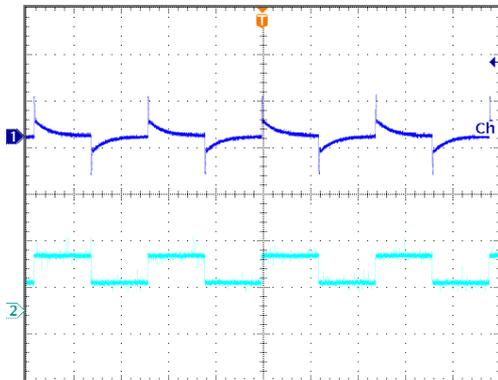
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General	Symbol	Min	Typ	Max	Unit	Conditions
MTBF	-	-	1.5×10^6	-	h	BELLCORE TR-332
Hand Soldering	Maximum soldering Temperature < 425°C, and duration < 5s					
Wave Soldering	Maximum soldering Temperature < 255°C, and duration < 10s					
Weight	-	-	69	-	g	-

Characteristic Curves

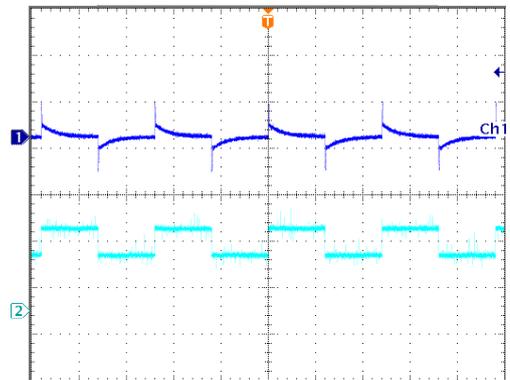
Load Transient Response



Load change: 25%~50%
 ~25% $I_{O,max}$, 0.1A/ μ s
 $V_{in}=24V$

Trace1: 500mV/div
 Trace2: 1.2A/div
 Time scale: 10ms/div

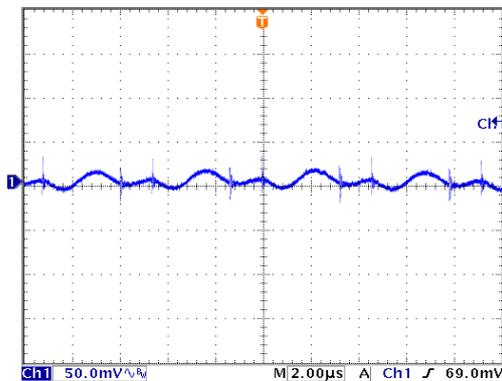
Load Transient Response



Load change: 50~75%
 ~50% $I_{O,max}$, 0.1A/ μ s
 $V_{in}=24V$

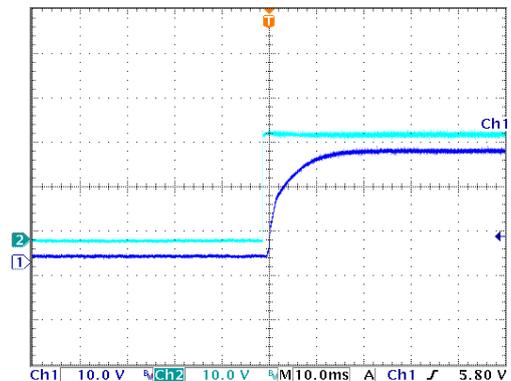
Trace1: 500mV/div
 Trace2: 1.2A/div
 Time scale: 10ms/div

Output Ripple and noise



$V_{in}=24Vdc$, $I_O=3.1A$ (20MHz)

Start-up Delay Time

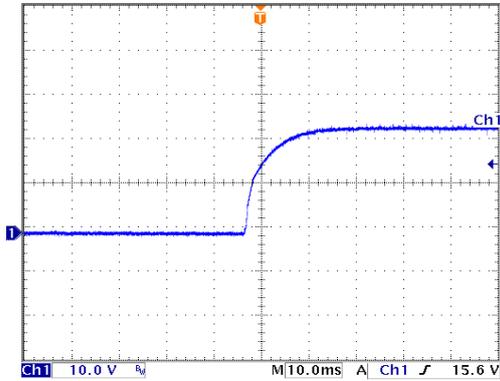


$V_{in}=24Vdc$, $I_O=3.1A$ (20MHz)

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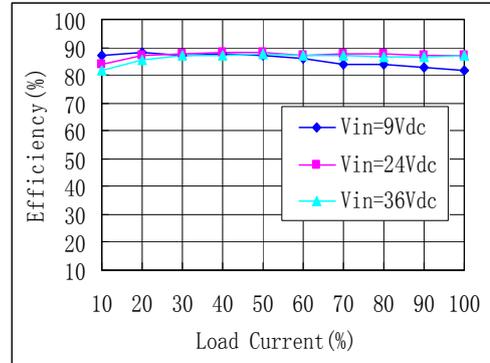
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Rise Time

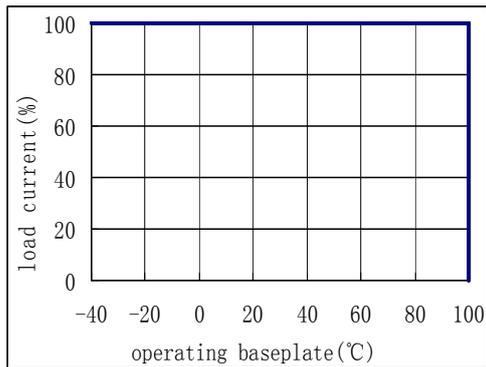


Vin=24Vdc, Io=3.1A

Efficiency vs. Io & Vin

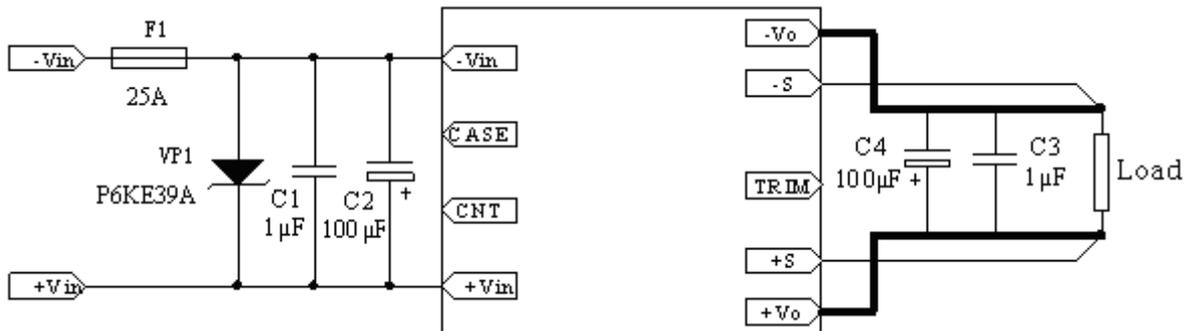


Derating



Design Considerations

Basic Connection

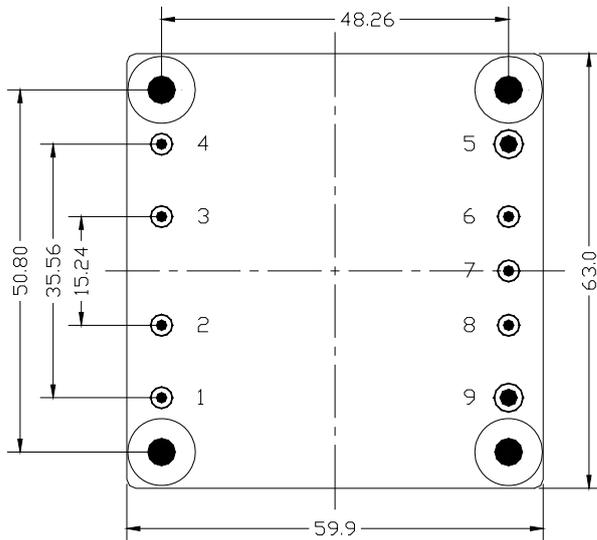


Notes: The basic connection indicates the basic requirements. Please refer to the instruction followed for further information.

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Recommended Layout

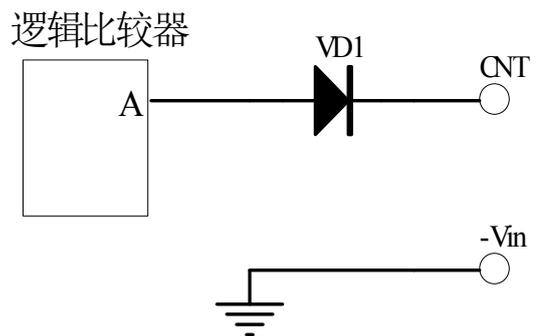


NO.	Recommendation & Notes
Pad Design	1-4、6-8 Pad holes: 1.5mm, pad diameter including hole:2.5mm; 5、9 Pad holes: 2.5mm,pad diameter including hole:4.50mm; the fixed holes at the four corners are metallized, with diameter of 3.3mm and pad diameter including hole: 5.3mm-6.3mm.
Airflow Direction	The air should flow along the direction of the heat sink
Safety	Isolated Converters,care to the spacing between input and output, input and protective ground、output and protective ground.
Electrical	The Vin(-) and Vo(-) planes should be placed under of the converter separately. Avoid routing sensitive signal or high disturbance AC signal under the converter.

Remote Control

Remote control can be offered by setting right control voltage level (refer to -Vin pin)to CNT pin.

Positive Logic Control: When the level is higher than 3.5V or be left floating,the converter will be on. When the level is less than 1.5V,the converter will be off. The circuit diagram is shown as “ internal circuit diagram for positive logic control”; when low level applied, the CNT source current is less than 1Ma, due to VD1 is signal diode, and the logic comparator is semiconductor integrated chip with low resistance to surge. Care should be taken to prevent CNT from surge, A TVS should be used in some cases.



Internal circuit diagram for positive logic control

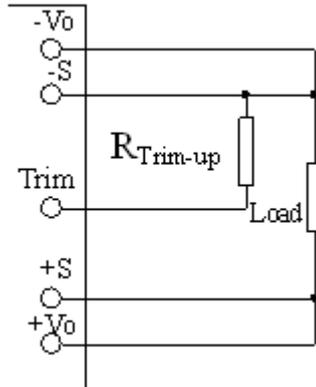
Output Voltage Adjust

The converters have an Output Voltage adjust pin (Trim). This pin can be used to adjust the output

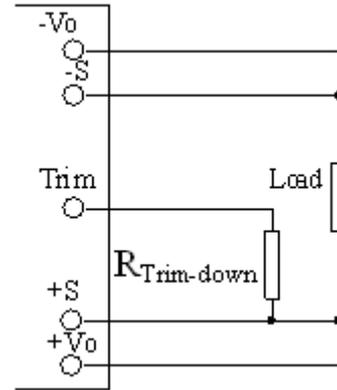
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voltage above or below Output voltage initial setting. When increasing the output voltage, the voltage at the output pins (including any remote sense offset) must be kept below the maximum output adjust range, or the characteristics will not be assured in compliant with the specification, even the over voltage protection may be triggered. Also note that at increased output voltages the maximum power rating of the converter 100W remains the same, and the output current capability will decrease correspondingly, at decrease output voltages the maximum current should not exceed 8.33A. When the trim pins are not used, they should be floated.



Connection for Trimming Up



Connection of Trimming Down

External circuit is connected as the figure shown, the resistance is calculated as the formula below, please note that the formula will be invalid when $R_{Trim-up}$ 、 $R_{Trim-down}$ are used simultaneously, users adjust the value based on the resistance applied.

$$\text{Resistance for trimming up : } R_{Trim-up} = \left(\frac{53.75}{\Delta V} - 15 \right) (k\Omega)$$

$$\text{Resistance for trimming down: } R_{Trim-down} = \left(\frac{(V_o - \Delta V - 2.5) \times 21.5}{\Delta V} - 15 \right) (k\Omega)$$

V_o : rated The output voltage you need, V;

$R_{Trim-up}$ 、 $R_{Trim-down}$: Resistance for trimming up or down, k Ω ;

Input Voltage Range

The input voltage range of the DC/DC converter is 9V~36V. The input impedance of the converter looks like a negative resistor, which can interact with the reactance of the power bus (including any filter elements that have been added to the input of the converter), causes an unstable condition. Depending on the internal transformer's impedance, the external impedance usually should not exceed the 10% of the internal.

Remote Sense

The remote sense can be used to compensate for the voltage drop between the output pins of the converter and the load input pins by +S、-S pins. The +S and -S pins should be connected to the input pins of the load respectively. The remote sense circuit will compensate for up to 0.5V drop between the

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sense voltage and the voltage at the output pins. If the remote sense is not needed, the -S should be connected to -Vo and +S should be connected to +Vo.

The anti-interference design should be considered when the +S、-S pins are connected to the pins to be compensated. The +S、-S traces should be located close to a ground trace or ground plane, and the area they surrounded should be minimized (just for electrical isolation); If cable connection presents, twisted pair wires should be used, EMI core are equipped with the twisted pair wires to reduce common mode noise when necessary, the sense leads should not be longer than 200mm, or the system characteristics may not be assured.

The sense leads only can carry very little current, and are not used for converter power output. Care should be taken in operation to avoid damaging the converter.

Over Temperature Protection(OTP)

The regulators are protected from thermal overload by an internal over temperature shutdown circuit. When the baseplate temperature exceeds the temperature trig point, the OTP circuit will cut down output power. The regulator will stop until safe operating temperature is restored. Hysteresis temperature between OTP trig point and restart is approx 10°C. Time between OTP and restart is dependent on cooling of the regulator.

Output Over Voltage Protection(OVP)

The clamp type over voltage protection feature is used to protect the converter, when output voltage exceeds 120% to 140% of the rated output voltage (the set point is between 115%-140%, there is the difference based on the specific parameters, but not beyond the range), the output voltage will clamped.

Safety Consideration

The converter, as one component for the end user, should be installed into the equipment, and all the safety considerations are achieved under certain condition. It is required to meet safety requirements in system design for the user. The converter input is considered TNV2, the primary to secondary is basic insulation to EN60950. The maximum operating temperature for PCB is 130 °C.

To avoid fire and be protected when short circuit occurred, it is recommended that a fast blow fuse with rating 2 -3 times of converter continuous input peak current is used in series at the input terminal. (Inrush current suppression circuit is required for greater filter capacitance at input terminal, or it will result in the misoperation of the fuse).

Series and Parallel Operation

The converters should not be paralleled directly to increase power, but they can be paralleled each other through o-ring switches or diodes. Make sure that every converter's maximum load current should not exceed the rated current at anytime, if they are paralleled without using external current sharing circuits. The converters can operate in series. To prevent against start-up failure due to start up time difference, SBD with low voltage difference can be paralleled at the output pins (SBD negative terminal connect to the positive pin of the output) for each converter.

Cleaning Notice

The converter is suitable for water washing, because it does not have any pockets where water could be trapped long-term. Users should ensure that the drying process is adequate and of sufficient duration to

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remove all water from the converter after washing, do not power up the unit until it is completely dry.

Delivery Package Information

Package material is multiple wall corrugated , internal material is anti-static foam, it's surface resistance is from $10^5 \Omega$ to $10^{12} \Omega$ 。 Tray capacity: 2×6=12 PCS/box , Tray weight: 0.94kg ; Carton capacity:15×12=180 PCS, Carton weight:14.6kg.

Quality Statement

The converters are manufactured in accordance with ISO-9001 system requirements, in compliant with YD/T1376-2005, and are monitored 100% by auto-testing system, 100% burn in.

The warranty for the converters is 5-year.

Contact Information

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